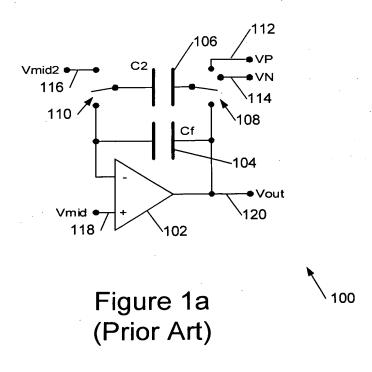
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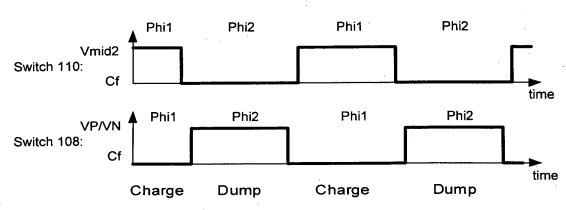


Figure 1b

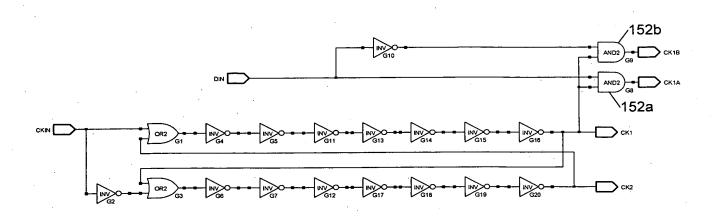
	Connected to:	
Switch	During Phi1 (Charge)	During Phi2 (Dump)
110	Vmid2	Cf
108	VP/VN	Cf

Figure 1c

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Figure 1d

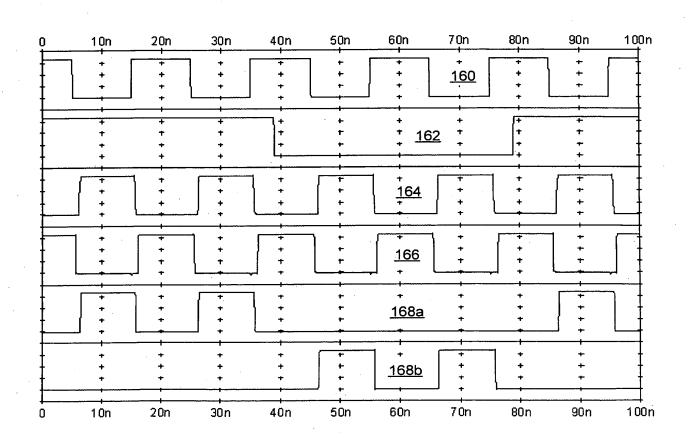


Figure 1e

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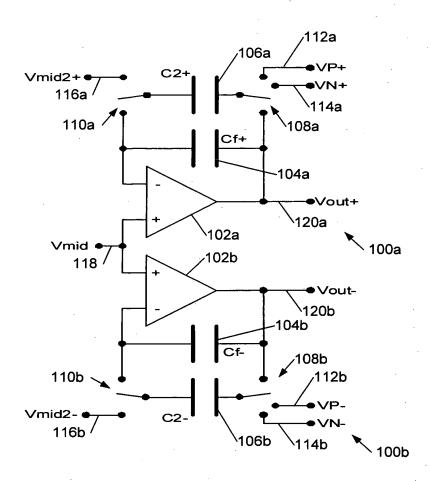


Figure 2 (Prior Art)

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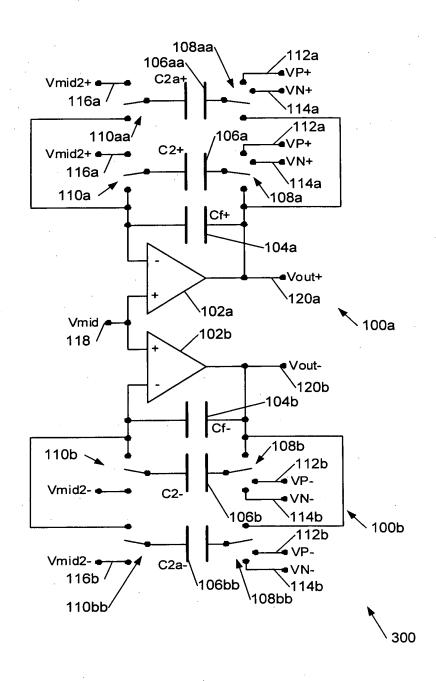


Figure 3 (Prior Art)

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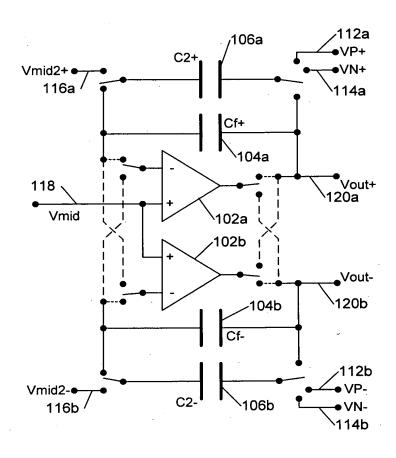


Figure 4

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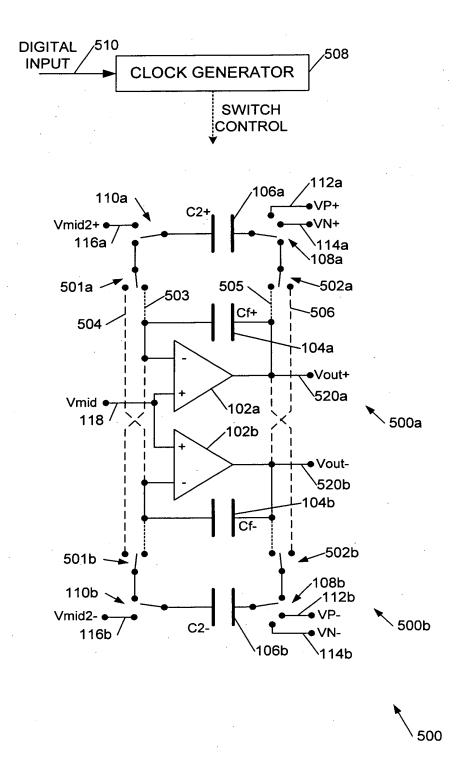


Figure 5a

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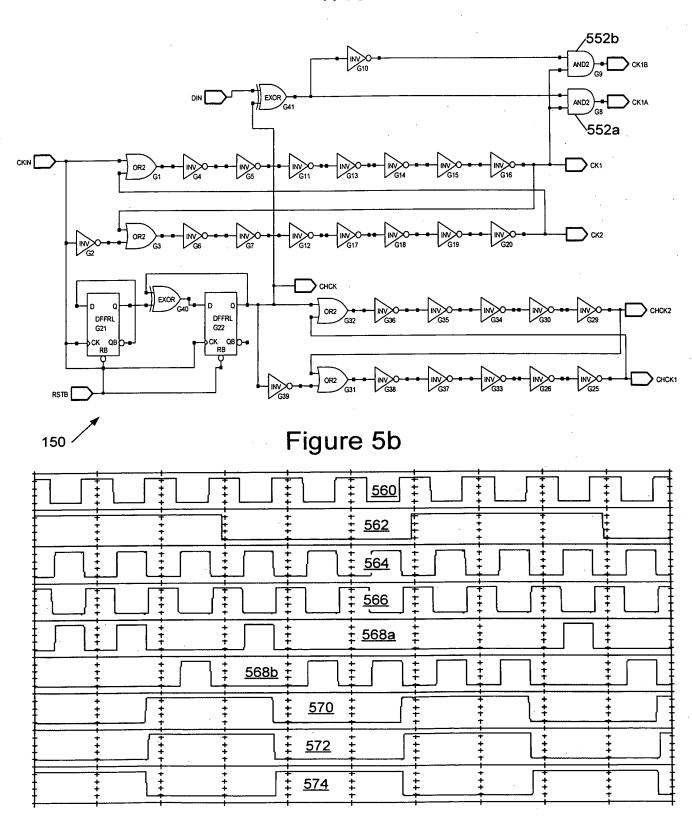


Figure 5c

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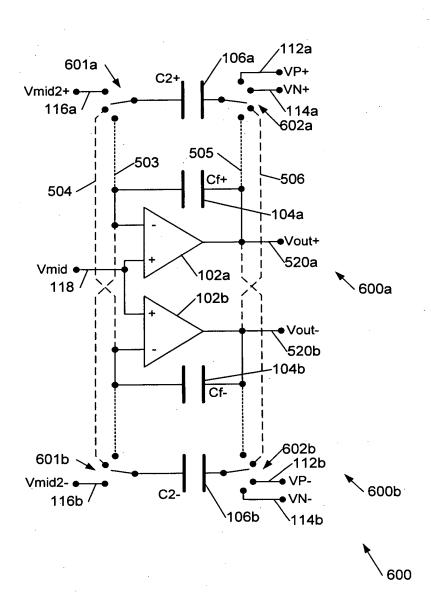


Figure 6

App No.: Not Yet Assigned Docket No.: M0025.030 Inventor: Peter J. Frith et al. Title: DIGITAL-TO-ANALOGUE CONVERTER CIRCUITS

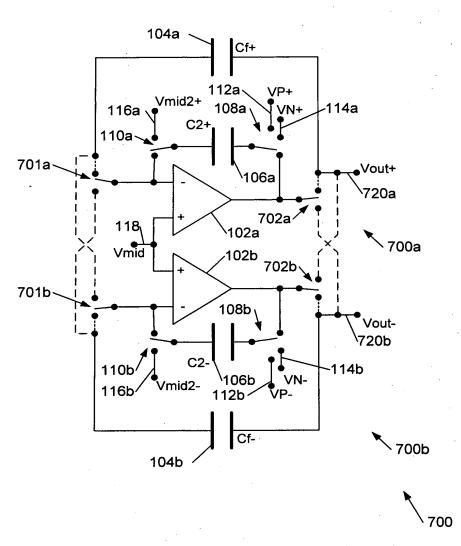


Figure 7

App No.: Not Yet Assigned Docket No.: M0025.030 Inventor: Peter J. Frith et al. Title: DIGITAL-TO-ANALOGUE CONVERTER CIRCUITS

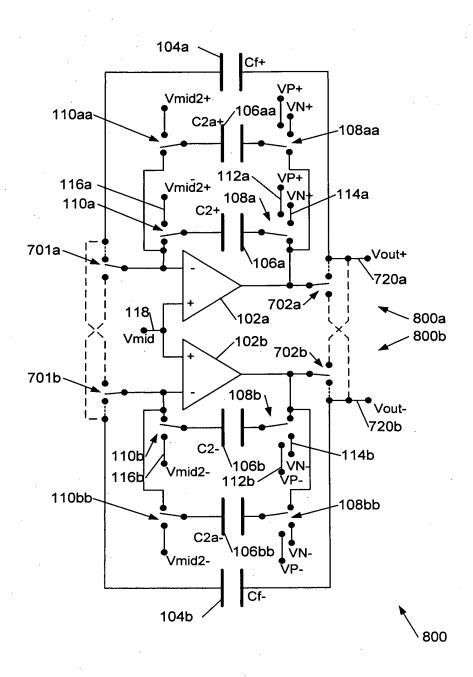


Figure 8